Name:

First Inventor or Application Identifier Toshihiro SEKIGAWA, et al.

Registration No.:

U.S. 18432 18432

or new nonprovisional applications under 37 CFR 1.53(b)

Title METHOD FOR APPLICATION OF GATING SIGNAL IN DOUBLE GATE FET

See MPEF	APPLICATION ELI chapter 600 concerning utility	Commissioner for Patents ADDRESS TO: Mail Stop Patent Application Alexandria, Virginia 22313						
	Fransmittal Form (e.g. Fit an original and a duplicate for	ACCOMPANYING APPLICATION PARTS 7. Assignment Papers (cover sheet & document(s))						
2. Spec	ification	Total Sheets 14	8. Applic	ation Data Sh	eet. See	37 CFR 1.76	Ì	
			9. 🗆 37 C.F	R. §3.73(b) s nere is an assigne	Statement e)	□ Power of Attorney		
3. Drav	ring(s) (35 U.S.C. 113)	Total Sheets 8	_			t (if applicable)		
				ation Disclosu nent (IDS)/PT		□ Copies of Citations	IDS	
4. Oath	or Declaration	Total Pages 4	12. Prelim	=				
a.	Newly executed (origin	13. White Advance Serial No. Postcard						
b. 🗆	(for continuation/divisional wi		14. Certified Copy of Priority Document(s) (if foreign priority is claimed)					
		NVENTOR(S) sched deleting inventor(s) named in see 37 C.F.R. §1.63(d)(2) and		ant claims sm CFR 1.27	all entity s	status.		
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For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.								
		18. CORRESPOND	ENCE ADDRE	SS				
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Signature: /Wm/NGr		lland		Date:	3/25/04			

C. Irvin McClelland
Registration Number 21,124



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Toshihiro SEKIGAWA, et al.

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FOR:

METHOD FOR APPLICATION OF GATING SIGNAL IN DOUBLE GATE FET

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	Respectfully Submitted,
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Da	e: 3/25/04 ChmMbrllen
	Marvin J. Spivak
_	Registration No. 24,913

A duplicate copy of this sheet is enclosed.

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